

JUL 02 2004

PTO/SB/08B (08-03)

 INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
Sheet 1		of 1	Application Number	10/784,613	
			Filing Date	February 23, 2004	
			First Named Inventor	Tsividis, Yannis	
			Art Unit	2685	
			Examiner Name	R. Vuong	
			Attorney Docket Number	021673-000210US	

U.S. PATENT DOCUMENTS+					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
AV	AA	US-2002/0036519	03-28-2002	Krishnapura et al.	
AV	AB	US-2003/0112059	06-18-2003	Krishnapura et al.	
AV	AC	US-2004/0042572	03-04-2004	Palaskas et al.	
AV	AD	US-2004/0091035	05-13-2004	Palaskas et al.	
	AE	US-			

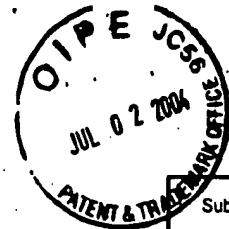
FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)			
	AF				<input type="checkbox"/>
	AG				<input type="checkbox"/>

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
AV	AH	ARYA R. BEHZAD et al., "A 5-GHz Direct-Conversion CMOS Transceiver Utilizing Automatic Frequency Control for the IEEE 802.11a Wireless LAN Standard", IEEE Journal of Solid-State Circuits, Vol. 38, No. 12, December 2003			
AV	AI	VINCENT LEUNG et al., "Digital-IF WCDMA Handset Transmitter IC in 0.25um SiGe BiCMOS", 2004 IEEE International Solid-State Circuits Conference			
AV	AJ	MASOUD ZARGARI et al., "A Single-Chip Dual-Band Tri-Mode CMOS Transceiver for IEEE 802.11a/b/g WLAN", 2004 IEEE International Solid-State Circuits Conference			
	AK				
	AL				

Examiner Signature	<i>Ruthven Vuong</i>	Date Considered	12/17/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.



Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

Complete if Known

Application Number	10/784,613
Filing Date	02/23/2004
First Named Inventor	Tsividis, Yannis
Art Unit	2685
Examiner Name	R. Vuong

Attorney Docket Number 021673-000210US

U.S. PATENT DOCUMENTS+

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		Number Kind Code ² (if known)			
✓	AA	US-4,270,222	05-26-1981	Menant	
	AB	US-5,001,776	03-19-1991	Clark	
	AC	US-5,390,357	02-14-1995	Nobusawa	
	AD	US-5,392,457	02-21-1995	Davis et al.	
	AE	US-5,406,613	04-11-1995	Peponides et al.	
	AF	US-5,564,094	10-08-1996	Anderson et al.	
	AG	US-5,600,271	02-04-1997	Erickson et al.	
	AH	US-6,026,288	02-15-2000	Bronner	
	AI	US-6,369,445	05-14-2002	Tsividis	
	AJ	US-6,400,177	06-04-2002	Yoshizaki	
	AK	US-6,529,069	03-04-2003	Krishnapura et al.	
	AL	US-20030112060	08-19-2003	Krishnapura et al.	
	AM	US-20030117212	08-26-2003	Krishnapura et al.	
✓	AN	US-6,683,492	01-27-2004	Krishnapura et al.	
	AO	US-			

FOREIGN PATENT DOCUMENTS

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		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
✓	AP	PCT	WO 02/35756	05-02-2002	Yoshizawa et al.		<input type="checkbox"/>
✓	AQ	PCT	WO 02/056558	07-18-2002	Palaskas et al.		<input type="checkbox"/>
✓	AR	PCT	WO 02/056470	07-18-2002	Palaskas et al.		<input type="checkbox"/>
	AS						<input type="checkbox"/>

Examiner Signature

Arthur Vuong

Date Considered

12/17/04

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AV	AT	P.R. GRAY et al., "Future Directions in Silicon ICs for RF Personal Communications", IEEE 1995 Custom Integrated Circuits Conference		
	AU	Y. TSIVIDIS, "Minimising power dissipation in analogue signal processors through syllabic companding", Electronic Letters, Vol. 35, No. 21, October 14, 1999		
	AV	AARNO PARSSINEN et al., "A 2-GHz Wide-Band Direct Conversion Receiver for WCDMA Applications", IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999		
	AW	FARBOD BEHBAHANI et al., "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000		
	AX	TUOMAS HOLLMAN et al., "A 2.7-V CMOS Dual-Mode Baseband Filter for PDC and WCDMA", IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001		
	AY	FARBOD BEHBAHANI et al., "Adaptive Analog IF Signal Processor for a Wide-Band CMOS Wireless Receiver", IEEE Journal of Solid-State Circuits, Vol. 36, No. 8, August 2001		
	AZ	ATUSHI YOSHIZAWA et al., "An Anti-Blocker Structure MOSFET-C Filter for a Direct Conversion Receiver", IEEE 2001 Custom Integrated Circuits Conference		
	BA	JUSSI RYYNANEN et al., "RF Gain Control in Direct Conversion Receivers", 2002 IEEE		
	BB	ALEKSANDAR TASIC et al., "Concept of Frequency-Transconductance Tuning of Bipolar Voltage-Controlled Oscillators", 2002 IEEE		
	BC	ALEKSANDER TASIC et al., "Concept of Phase-Noise Tuning of Bipolar Voltage-Controlled Oscillators", 2002 IEEE		
	BD	Y. TSIVIDIS et al., "Internally Varying Analog Circuits Minimize Power Dissipation", IEEE Circuits & Devices Magazine, January 2003		
AV	BE	CHRIS VAN DEN BOS et al., "Architecture of a Reconfigurable Radio Receiver Front-End Using Overall Feedback", Electronic Research Laboratory, Delft University of Technology		

Examiner Signature	R. Vuong	Date Considered	12/17/04
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